## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Averett, Guy E. et al. : Examining Group: 2811

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Serial No.: 10/072,145

Filed: 02/07/2002 : Examiner: Ori Nadav

For: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS
OF LOW SUBSTRATE CAPACITANCE

## SUBMISSION OF AMENDMENT IN CONJUNCTION WITH A REQUEST FOR CONTINUING EXAMINATION UNDER 37 CFR 1.114

Honorable Commissioner of Patents, P.O. BOX 1450 Alexandria, VA 22313-1450

SIR:

Applicants are submitting this amendment ("Submission") in conjunction with a request for continuing examination under 37 CFR 1.114. This Submission is in response to the final rejection mailed on October 19, 2007. Please reconsider the above-identified application in view of the amendments and remarks presented hereinafter.